

### International Journal of Engineering

Journal Homepage: www.ije.ir

# Universal Filtered Multicarrier Receiver Complexity Reduction to Orthogonal Frequency Division Multiplexing Receiver

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#### PAPER INFO

ABSTRACT

Paper history: Received 12 November 2021 Received in revised form 08 January 2022 Accepted 09 January 2022

Keywords: Fifth Generation Bit Error Rate Complexity Reduction Ratio Fast Fourier Transform Finite Impulse Response Signal to Noise Ratio Universal Filtered Multicarrier The Universal Filtered Multicarrier (UFMC) waveform technology is one of the promising waveforms for 5G and beyond 5G networks. Owing 2N-point Fast Fourier Transform (FFT) processor at the UFMC receiver, the computational and implementation complexity is two times more than the conventional Orthogonal Frequency Division Multiplexing (OFDM) receiver system. In this paper, we proposed a simplified UFMC receiver structure to reduce computational complexity as well as hardware requirements. The received UFMC symbol simplified exactly to its equivalent after performing 2N-point FFT and decimation operations. In which, the mathematical model of the frequency-domain UFMC signal is rederived after processing through 2N-point FFT and decimator, and the simplified signal is generated with a N-point FFT. Accordingly, the 2N-point FFT processor and decimator are replaced with a single N-point FFT processor. This approach reduces the 50% computational complexity at the FFT processor level hence the hardware and processing time. The computational complexity of the proposed receiver model is approximately equivalent to the OFDM receiver. Additionally, analyzed the mathematical model for the simplified UFMC receiver and the comparative performance of the UFMC system with the conventional model.

doi: 10.5829/ije.2022.35.04a.12

NOMENCLATURE			
$f_p[l]$	The impulse response of p <sup>th</sup> sub-band filter	$Y_{2N}[k]$	2N-point DFT of $y_{i,zp}[n]$
f[l]	Desired/ideal filter impulse response	$W_N$	The twiddle factor of N-point DFT $W_N = e^{-\frac{j2\pi}{N}}$
h[n]	The impulse response of the wireless channel	$L_f$	Length of the sub-band filter
$S_{ip}[k]$	p <sup>th</sup> sub-band input constellation mapped data sequence of i <sup>th</sup> UFMC symbol	$L_h$	Length of the wireless channel
$s_{ip}[n]$	The output of N-point IFFT block of pth sub-band	Ν	Size of the IFFT block
$x_i[n]$	The final i <sup>th</sup> UFMC symbol	N <sub>b</sub>	Number of sub-bands
$x_{ip}[n]$	pth sub-band filter output of ith UFMC symbol	Q	Number of data subcarriers per sub-band
$y_i[n]$	The received ith UFMC symbol	V <sub>sc</sub>	Half of the virtual sub-carriers
$y_{i,zp}[n]$	The received i <sup>th</sup> UFMC symbol after zeros padded to y <sub>i</sub> [n]		

### **1. INTRODUCTION**

The ongoing fifth-generation (5G) systems continue to reveal the inherent limitations due to the rise of intelligent communication environments, and some 5G application use cases like massive machine-type communications

\*Corresponding Author Institutional Email: <u>*rmanda@ddn.upes.ac.in*</u> (R. Manda) (mMTC), and ultra-reliable low latency communications (URLLC) [1]. These limitations are motivation to define the technical requirements and targets of the nextgeneration (6G) cellular networks that can transfer beyond personalized communication toward the full realization of the Internet of Things (IoT) standard, that

Please cite this article as: R. Manda, R. Gowri, Universal Filtered Multicarrier Receiver Complexity Reduction to Orthogonal Frequency Division Multiplexing Receiver, *International Journal of Engineering, Transactions A: Basics,* Vol. 35, No. 04, (2022) 725-731 connects everything, not just people, but also sensors, vehicles, wearables, computing resources, and even robotic agents [2–4]. Therefore, 5G and beyond 5G network use cases complicate the required specifications in many aspects such as data rate, delay or latency, reliability, energy consumption, multicast connectivity, and the type of protocols that provides diverse ways to exchange information between the devices [5]. These systems are impacted by the modulation format used at the physical layer [6–8].

In the last few years, several physical layer waveforms have been proposed like Filter bank multicarrier (FBMC) [9], generalized frequency division multiplexing (GFDM) [10], filtered orthogonal frequency division multiplexing (F-OFDM) [11], and universal filtered multicarrier (UFMC) [12]. Out of these, the UFMC waveform technique is one of the competitive modulation schemes for future generation wireless systems to provide flexible packet transmission services, interference due to OBE, and relaxed low synchronization [13, 14]. However, the transmitter and receiver complexity of UFMC is higher compared to the conventional (CP-OFDM) due to its filtering operation at the transmitter and 2N-point FFT processor at the receiver. To reduce the system complexity and to improve the energy efficiency, the baseband signal processing time and hardware requirement for the new modulation waveform can be reduced by simplifying the structure of the transceiver architecture in terms of computations. In recent years, some methods have been proposed to reduce the computational complexity of the UFMC transmitter [15–18]. The transmitter complexity was reduced by approximating the frequency domain UFMC signal [15], by introducing the FIR filter structure and the poly-phase filter structure based on the lightweight method into the UFMC transmitter structure [17]. A reduced hardware complexity solution [16] was proposed for the implementation of IFFT and filtering operation by avoiding redundant computations. Recently, reconfigurable baseband UFMC а transmitter architecture was proposed by Kumar et al. [18], which has the flexibility to choose the number of subcarriers per sub-band and the pulse-shaping filter. Wu et al. [19] have proposed an advanced receiver for UFMC, which uses the odd number samples of 2N-point FFT along with even number samples to improve the performance at high computations, and here the computational complexity burden of 2N-point FFT has not been reduced. The FFT pruning approach [20], in which removing the operations related to the zero inputs reduces the computational complexity of the UFMC transmitter and receiver. In this paper, the method mainly focused on the system complexity, baseband signal processing latency, and power consumption at the receiver. The proposed approach simplifies the UFMC receiver model, which uses a single N-point FFT to generate the frequencydomain baseband signal for data detection like the conventional OFDM. This approach avoids the zeropadding operation and decimation part at the UFMC receiver baseband signal processing. Therefore, UFMC reduces the hardware requirement, computational complexity, and hence latency and power consumption. The FFT pruning approach at the UFMC receiver gives fewer computations compared to the conventional model but requires high-level reprogramming since the nonzero inputs vary over time. However, the proposed model requires a smaller number of computations compared to the conventional and the FFT pruning algorithm [20] which is clearly explained in the result analysis session. One of the minor drawbacks of the proposed model is that the hardware implementation needs to use L<sub>f</sub>+L<sub>h</sub>-2 number of adders before the N-point FFT, which may cause the rise of connectivity complexity and word length effect when it processes through the FFT processor. Otherwise, the proposed model is superior to the conventional model.

The rest of the paper is organized as follows; section 2 described the conventional UFMC transceiver model, section 3 explained the proposed UFMC receiver model that how it is derived from the traditional UFMC receiver model, and section 4 discusses the computational complexity. Section 5, described the performance analysis in terms of complexity and SNR versus BER, and finally, concluded in section 6.

### 2. CONVENTIONAL UFMC SYSTEM MODEL

**2. 1. The UFMC Transmitter Model** The UFMC waveform technique is a generalized form of OFDM and FBMC, in which a group of subcarriers is individually filtered with a bandpass filter [12]. The schematic block diagram of the traditional UFMC transceiver model is depicted in Figure 1. Whereat the UFMC transmitter, the entire frequency band (total subcarrier) is divided into several blocks (a group of sub-carriers) called sub-band and filtered individually. The final  $i^{th}$  UFMC timedomain symbol is the sum of the outputs of all sub-band filters, which can be expressed as

$$x_i[n] = \sum_{p=0}^{N_b - 1} x_{ip}[n]$$
(1)

The  $p^{\text{th}}$  sub-band filter output of  $i^{\text{th}}$  UFMC symbol

$$x_{ip}[n] = \sum_{l=0}^{L_f - 1} f_p[l] s_{ip}[n - l]; n = 0, 1, 2, \dots, N + L_f - 2$$
(2)

where  $f_p[l]$ ;  $l = 0, 1, ..., L_f - 1$  is  $p^{\text{th}}$  sub-band filter impulse response, which is the frequency-shifted version of prototype filter f[l] to the center of  $p^{\text{th}}$  sub-band.

$$f_p[l] = f[l]e^{\frac{j2\pi}{N}\left(V_{sc} + \left(p - \frac{1}{2}\right)Q + \frac{N}{2}\right)l}$$
(3)



Figure 1. Traditional UFMC transceiver model

The output of the N-point IFFT  $s_{ip}[n]$  can be written as

$$s_{ip}[n] = \frac{1}{N} \sum_{k=0}^{N-1} S_{ip}[k] e^{j\frac{2\pi}{N}kn} ; n = 0, 1, \dots, N-1$$
(4)

The final UFMC symbol  $x_i[n]$  with the length of  $N + L_f - 1$  transmitted through the wireless channel, which is

$$y_i[n] = x_i[n] * h[n] + z[n]; n = 0, 1, ..., N_T - 1$$
(5)

where z[n] is a complex-valued Additive White Gaussian noise (AWGN) with zero mean and variance of  $\sigma_z^2$ ,  $N_T = N + L - 2$  and  $L = L_f + L_h$ .

**2.2. The UFMC Receiver Model** At the receiver, the zeroes padded to the received symbol to perform the 2N-point FFT to consider the sub-band filter tails. The received signal after zeros padded

$$y_{i,zp}[n] = \begin{cases} y_i[n]; & 0 \le n \le N_T - 1\\ 0; & N_T \le n \le 2N - 1 \end{cases}$$
(6)

The frequency-domain of the received UFMC signal after processing through the 2N-point FFT is mathematically formulated as

$$Y_{2N}[k] = \sum_{n=0}^{2N-1} y_{i,zp}[n] W_{2N}^{kn}; \ k = 0, 1, ..., 2N - 1$$
(7)

The receiver only needs to extract the even-numbered samples after the 2N-point FFT transformation to estimate the data symbols because the odd-numbered samples consist of an interference component (proof can see in the appendix). In conventional UFMC receiver, this is implemented with 2N-point FFT and decimator with factor 2 shown in Figure 1, which is twice the size of FFT that is used in conventional OFDM receiver and increases the UFMC receiver complexity compared to the OFDM receiver.

**2.3. Data Detection** The final UFMC transmitted symbol x(n) having the length  $N + L_f - 1$ , can be expressed in matrix form as

$$X = \sum_{p=1}^{B} \left[ F_p \right]_{\left(N+L_f-1\right) \times N} \left[ V_p \right]_{N \times Q} \left[ S_p \right]_{Q \times 1}$$
(8)

where  $[F_p]_{(N+L_f-1)\times N}$  is a Toeplitz matrix of  $p^{\text{th}}$  subband FIR filter impulse response with the first column  $[f_p(0), f_p(1), \dots, f_p(L_f-1), [0]_{1\times N-1}]^T$  and first row  $[f_p(0), [0]_{1\times N-1}], [V_p]_{N\times Q}$  is the IFFT matrix that relevant to  $p^{\text{th}}$  sub-band carriers and  $S_p$  is the column matrix of the  $p^{\text{th}}$  sub-and data sequence.

$$X = \mathcal{FVS}$$

$$F = [F_0, F_1, \dots, F_{B-1}]$$

$$V = blockdiag([V_0, V_1, \dots, V_{B-1}])$$

$$S = [S_0^T, S_1^T, \dots, S_{B-1}^T]^T$$
(9)

This UFMC symbol is transmitted through the wireless channel, which can be defined as

$$y = HX + z = \mathcal{HFVS} + z \tag{10}$$

where  $\mathcal{H}$  is the Toeplitz matrix of the channel coefficients h(n) with the first column [h(0), h(1), ..., n]

$$h(L_h - 1)$$
,  $[0]_{1 \times N + L_f - 2}$  and first row  $[h(0), [0]_{1 \times N + L_f - 2}]$  and  $z$  is the zero mean Additive White Gaussian Noise (AWGN) vector having length  $N + L - 2$ . After performing the 2N-point FFT, the even-numbered frequency domain samples are

$$Y_e = \mathcal{P}_e \mathcal{WHFVS} + \mathcal{P}_e^T \mathcal{WZ} \tag{11}$$

where the matrix  $[\mathcal{P}_{e}]_{N \times 2N}$  with elements

$$\mathcal{P}_{e}(m,k) = \begin{cases} 1; & \text{for } k = 2m\\ 0; & \text{for } k \neq 2m \end{cases}$$
(12)

where m = 0, 1, ..., N - 1 and k = 0, 1, ..., 2N - 1Where  $[\mathcal{W}]_{2N \times (N+L-2)}$  are the 2N-point FFT twiddle factor matrix and its elements are  $\mathcal{W}(k, r) = W_{2N}^{kr}$ ; where k = 0, 1, ..., 2N - 1; and r = 0, 1, ..., N + L - 3. These even-numbered samples are used for data detection. By Least squares algorithm, the estimated data sequence is defined as

$$\hat{S} = (A^H A)^{-1} A^H Y_e; A = \mathcal{P}_e^T \mathcal{WHFV}$$
(13)

### **3. THE PROPOSED UFMC RECEIVER MODEL**

The even-numbered samples/sub-carriers of 2N-point FFT are defined as

$$Y_e[k] = Y_{2N}[2k] = \sum_{n=0}^{2N-1} y_{i,zp}[n] W_{2N}^{2kn}; k = 0, 1, \dots, N-1$$
(14)

We know that

$$W_{2N}^{2kn} = e^{\frac{-j2\pi}{2N}2kn} = e^{-\frac{j2\pi}{N}kn} = W_N^{kn}$$
(15)

We rewrite Equation (8) as

$$Y_{e}[k] = \sum_{n=0}^{2N-1} y_{i,zp}[n] W_{N}^{kn} = \sum_{n=0}^{N-1} y_{i,zp}[n] W_{N}^{kn} + \sum_{n=N}^{2N-1} y_{i,zp}[n] W_{N}^{kn}$$
(16)

Let assume that m = n - N, then, Equation (16) can be rewritten as

$$Y_{e}[k] = \sum_{n=0}^{N-1} y_{i,zp}[n] W_{N}^{kn} + \sum_{m=0}^{N-1} y_{i,zp}[m + N] W_{N}^{k(m+N)}$$
(17)

where 
$$W_N^{\ kN} = e^{-\frac{j2\pi}{N}kN} = e^{-j2\pi k} = 1$$
  
 $Y_e[k] = \sum_{n=0}^{N-1} y_i[n] W_N^{\ kn} + \sum_{m=0}^{L-3} y_i[m + N] W_N^{\ km}$ 
(18)

Finally, Equation (18) can be written as

$$Y_e[k] = \sum_{n=0}^{N-1} y'[n] W_N^{kn}$$
(19)

where

$$y'[n] = \begin{cases} y_i[n] + y_i[n+N]; \ 0 \le n \le L-3\\ y_i[n]; \ L-2 \le n \le N-1 \end{cases}$$
(20)

Now, according to Equation (20) the even-numbered subcarriers can be generated with a single N-point FFT having y'[n] as input, which is shown in Figure 2. Here, both the 2N-point DFT and decimator operations can be implemented with the single N-point DFT. The computational complexity can be reduced twice compared to the conventional UFMC receiver.

## 4. COMPLEXITY ANALYSIS OF THE PROPOSED MODEL

The major computational complexity in the UFMC receiver system includes the 2N-point FFT processor, the channel estimation, and equalization algorithms. The UFMC baseband signal model at the receiver is like the OFDM signal except for the filter equalization, so, the same algorithms can be applied in the UFMC system for channel estimation and equalization. Therefore, the



Figure 2. Block diagram of the proposed UFMC receiver model

UFMC receiver complexity is two times higher compared to the OFDM receiver. Furthermore, the additional memory or control overhead is one of the main disadvantages of the implementation.

The N-point DFT is efficiently computed by the FFT algorithm [21], which requires the total number of arithmetic operations (real multiplications and additions) using radix-2 FFT algorithm is  $5N \log_2 N$ . The splitradix FFT algorithm [22] has a lower number of arithmetic operations compared to the radix-2 FFT algorithm, which requires the total number of arithmetic operations to be  $4N \log_2 N - 6N + 8$ . With the FFT pruning algorithm, the number of real operations (additions and multiplications) required to process the UFMC baseband received signal [20] is  $5N \log_2 N -$ 2N + 4(L - 2). Table 1 describes the computational complexity comparison between the conventional, FFT pruning approach and the proposed receiver model. Which states that the proposed UFMC receiver model has a smaller number of arithmetic operations, it is simple and effective compared to conventional CP-OFDM and UFMC receivers. The complexity efficiency of the system can be measured by complexity reduction ratio (CRR), which is defined as the ratio of the total number of computations required for the conventional model  $(CM_{ufmc}^{conv})$  to the total number of computations required for the conventional model  $(CM_{ufmc}^{prop})$ .

$$CRR = \frac{CM_{ufmc}^{conv}}{CM_{ufmc}^{prop}} \approx 2\left(1 + \frac{1}{\log_2 N}\right)$$
(21)

From Equation (21), the proposed model reduces the computational complexity more than two times as compared to the conventional UFMC receiver model. Also, in the proposed model, there are no zeros padding operations to process through 2N-point FFT. Therefore, the number of read/ write memory locations was reduced by two times approximately. However, the required storage space for read/ write operations is the same as the conventional OFDM receiver model. In the proposed

**TABLE 1.** Computational complexity comparison

Receiver model type	Number arithmetic operations (CM)	Required hardware blocks for the baseband signal processing at the receiver
CP-OFDM receiver	$4N\log_2 N - 6N + 8$	CP remover, N- point FFT, FDE
Conventional UFMC receiver	$8N\log_2 N - 4N + 8$	Zeros padder, 2N- point FFT, Decimator, FDE
UFMC receiver with FFT pruning	$5N \log_2 N - 2N + 4(L-2)$	Zeros padder, 2N- point FFT, FDE
Proposed UFMC receiver	$4N \log_2 N - 6N + 8 + 2(L-2)$	N-point FFT, FDE

model, the adder blocks are used before N-point FFT which may increase the connectivity complexity compared to the traditional model.

The power carried by odd-numbered frequency samples of 2N-point FFT is not utilized in the conventional UFMC receiver, hence the power efficiency is 50%. But in the proposed model, there is no 2N-point FFT and which uses all the samples processed by FFT. Therefore, the power efficiency can be improved to 100%. Finally, we can say that the proposed UFMC receiver model is more suitable for ultra-low latency and low energy consumption IoT uses cases as well as for next-generation cellular networks.

### **5. SIMULATION RESULTS**

The computational complexity of the receiver depends only on the size of the FFT. In this session, some computer simulation results are presented. The numerical analysis of computational complexity and its comparison is shown in Figure 3 for different bandwidth (BW) configurations mentioned in Table 2 under the NR-TDL vehicular-A channel model with a length of 24. These comparisons conclude that the proposed receiver model has a lesser number of arithmetic operations (i.e., two times lesser) at the baseband FFT signal processing level compared conventional model and almost achieved the same computational complexity of the CP-OFDM receiver. Furthermore, the complexity ratio (CR) of the  $\left(\frac{CM_{ufmc}}{CM_{ofdm}}\right)$  for UFMC receiver to the OFDM receiver different methods are shown in Figure 4. Consider the bandwidth of 20 MHz and N = 1024 for numerical comparison between the proposed and conventional models, in this case, the CR values are 2.1904, 1.3881, and 1.0036 for the conventional, conventional with FFT pruning algorithm and the proposed UFMC receiver model respectively. From these numerical analyses, the proposed model has less complexity ratio and is more



**Figure 3.** Comparison of computational complexity (with split-radix FFT algorithm)

**TABLE 2.** Bandwidth configuration for NR frequency band range (FR)-1 for SCS = 15 kHz [23]

Bandwidth/specifications	5 MHz	10 MHz	20 MHz
Number of subcarriers	333	666	1333
Data subcarrier	300	624	1272
FFT/IFFT size	512	1024	2048
CP length = $L_f - 1$	36	72	144

efficient in terms of computational complexity. Also, the FFT's per energy [24] can be improved, which is defined as

$$FFT's/Energy = \frac{Technology}{Power \times Execution \ time \times 10^{-6}}$$
(22)

where Technology is the CMOS process in micrometers, the power consumption is proportional to supply voltage, clock frequency, and load capacitance. The execution time depends on the number of operations/computations required to complete a particular task. From Equation (22), the FFT's per energy is inversely proportional to the execution time, which means for the proposed model the FFT processor requires lesser execution time compared to the conventional one. Finally, we can say that the proposed UFMC receiver model is more suitable for ultra-low latency and low energy consumption uses cases of the next-generation cellular networks.

The proposed model is a simplified model of the conventional model. Therefore, it gives almost the same performance in terms of SNR versus BER at lower computational cost, which is shown in Figure 5 for the simulation parameters mentioned in Table 3.

In practical cases due to the additional adders on the receiver side, the proposed model may increase the connectivity complexity and occurs small losses; hence small performance degradation (shown in Figure 5).



**Figure 4.** Complexity ratio (CR) of the UFMC receiver to the OFDM receiver



TABLE 3. Simulation parameters

Parameter name	Value		
Channel bandwidth	10 MHz		
Modulation type	16-QAM		
FFT size	1024		
CP length	72		
Number of UFMC symbols	7		
Sub-band size	12		
Stopband attenuation $(A_s)$	40 dB		
Channel type	AWGN/ Rayleigh fading		

### **6. CONCLUSION**

The UFMC was one of the candidate waveforms for 5G, but the computational and hardware complexity of the UFMC transceiver system has more than the CP-OFDM system due to filtering operation at the transmitter and 2N-point FFT processing at the receiver. In this point of view, here we proposed the simplified UFMC receiver model, in which the exact frequency domain UFMC received symbol after FFT processor and decimator is derived and simplified to implement with a single Npoint FFT and that reduced the computational complexity more than two times (i.e., 50%) compared to the traditional receiver model without degrading the system performance. At the receiver, the zero-padding for processing 2N-point FFT and decimation part is simply replaced by one N-point FFT, which reduced the number of hardware components at baseband signal processing and the storage requirement for read/ write operation to process the data and the number of computations or This model reduced the hardware operations. requirement and hence the power consumption. The realtime hardware implementation of this model is the future scope of this work.

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### **APPENDIX**

The frequency-domain received UFMC signal after processing through the 2N-point FFT

$$Y_{2N}(k) = \sum_{n=0}^{2N-1} y_{2p}(n) e^{-\frac{j2\pi}{2N}kn}$$
(23)

To detect the data from this received signal like in the OFDM system, we require to collect N- samples from 2N- samples and these samples do not contain any interference components. For this purpose, the 2N-samples are divided into even and add number samples.

For even-numbered samples, k = 0, 2, 2N - 2 and k = 2k'; k' = 0, 1, ..., N - 1 therefore, the evennumbered subcarriers of 2N-point FFT are

$$Y_e(k') = Y_{2N}(2k') = \sum_{n=0}^{2N-1} y_{zp}(n) e^{\frac{j2\pi}{N}k'n}$$
(24)

Similarly, the odd-numbered subcarriers are

$$Y_{o}(k') = Y_{2N}(2k'+1) = \sum_{n=0}^{2N-1} y_{zp}(n) e^{\frac{j2\pi}{2N}(2k'+1)n} = \sum_{n=0}^{2N-1} y_{zp}(n) e^{\frac{j2\pi}{2N}k'n} e^{\frac{j\pi}{N}n}$$
(25)

From Equations (23) and (24), it was observed that the data can be directly retrieved from the even-numbered subcarriers, but it cannot be retrieved from odd-numbered subcarriers due to the presence of interference component. That is why the odd-numbered subcarriers are discarded and even-numbered subcarriers are extracted for data detection at the receiver using a single-tap frequency domain equalizer (FDE).

### Persian Abstract

فناوری شکل موج چند حامل فیلتر شده جهانی (UFMC) یکی از شکلهای موج امیدوارکننده برای شبکههای Go و فراتر از آن است. با توجه به پردازشگر تبدیل فوریه سریع (FFT) CC در گیرنده OFDM) پیچیدگی محاسباتی و پیادهسازی دو برابر بیشتر از سیستم گیرنده متعارف تقسیم فرکانس متعامد (OFDM) است. در این مقاله، ما یک ساختار گیرنده OFDC در گیرنده OFDM ساده شده را برای کاهش پیچیدگی محاسباتی و همچنین نیازهای سخت افزاری پیشنهاد کردیم. نماد دریافتی UFMC دقیقاً پس از انجام عملیات یک ساختار گیرنده OFDC ساده شده را برای کاهش پیچیدگی محاسباتی و همچنین نیازهای سخت افزاری پیشنهاد کردیم. نماد دریافتی UFMC دقیقاً پس از انجام عملیات IFT تحلیف UFMC ساده شده را برای کاهش پیچیدگی محاسباتی و همچنین نیازهای سخت افزاری پیشنهاد کردیم. نماد دریافتی UFMC دقیقاً پس از انجام عملیات IFT تقطه SC و حذف دقیقاً به معادل خود ساده شد که در آن، مدل ریاضی سیگنال UFMC حوزه فرکانس پس از پردازش از طریق FFT نقطه SC و دسیماتور دوباره استخراج می شود و سیگنال ساده شده با یک FFT نقطه ST و UFMC با می سیگنال UFMC می دود از می به دود وزاره استخراج می شود و سیگنال ساده شده با یک FFT نقطه ST و UFMC با می دود. استخراج می شود. این رود و یک و در این اساس، پردازنده ST و در ST و یک و در ازده ST و در این می می دود. این می شود. استخراج می شود و سیگنال ساده شده با یک FFT نقطه ST و UFMC با و پردازش از طریق FFT نقطه ST و یک و در نتیجه زمان می شود. این رود و دود نتیجه زمان سخت افزار و پردازش را کاهش می دهد. پیچیدگی محاسباتی مدل گیرنده ST و تحلیل قرار گوینده ST و در نتیجه زمان سختافزار و پردازش را کاهش می دهد. پیچیدگی محاسباتی مدل گیرنده ST و تو در تیجه زمان سختافزار و پردازش را کاهش می دهد. پیچیدگی محاسباتی مدل گیرنده ST و تو در قیر می می می دود. پیزیده ST و تو در تو در تیز در ST و در نتیجه زمان سختافزار و پردازش را کاهش می دهد. پیچیدگی محاسباتی مدل گیرنده ST و تو در تیز می می می دود تیز و در تیزه ی در تو در و دو در تو در در تو در تو در تو در تو در تو پردازش را کاهش می دود. پیچیدگی محاسباتی دار در تو در در تو در تو در در در در تو در در تو در درو در در در تو در در درو در در در در در دو در تو در در در درو

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